

Remarks

Reconsideration and allowance in view of the above amendments and the following remarks are respectfully requested.

Claims 1-12 are rejected under 35 U.S.C. §103(a) over Fisher et al. (U.S. 6,026,479), hereafter "Fisher," in view of Dowling (U.S. 6,128,728). This rejection is defective because Fisher and Dowling, taken alone or in combination, fail to teach or suggest each and every feature of claimed invention.


Independent claims 1 and 8, as herewith amended, now additionally recite that in case of an interrupt the state of all of the limited subset of hardware resource means are stored within a single clock cycle using a plurality of flip-flops. It is respectfully submitted this feature is neither shown nor suggested in Fisher nor Dowling.

In the Final Office Action, the Examiner admits that Fisher fails to disclose that "all of said limited subset of hardware resource means are stored within a single clock cycle." To overcome this deficiency of Fisher, the Examiner relies on the teachings of Dowling. Contrary to the present invention, however, Dowling provides single-cycle register save and restore operations using "virtual memory sets in memory" rather than the claimed plurality of flip-flops.

In view of the foregoing, it is respectfully submitted that the currently-pending claims are in condition for allowance, and favorable consideration is earnestly solicited.

If the Examiner believes that any further discussion of the invention would be helpful, Applicants' representative is available at (518) 449-0044, and earnestly solicits such discussion.

Respectfully submitted,



By: John A. Merecki
Reg. No. 35,812

Date: 12/17/04

Hoffman, Warnick & D'Alessandro LLC
Three E-Comm Square
Albany, New York 12207
518-449-0044
jmerecki@hwdpatents.com